

35 U.S.C. §112 Rejection of Claims

In the June 5, 2001 Office Action, claims 31-34 and 36 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. In particular, antecedent basis informalities were identified in connection claims 31, 32, 34 and 36. Claims 31, 32, 34 and 36 have been amended and the instant section 112 rejections are now moot. Accordingly, the Applicants respectfully request that the instant section 112 rejections be withdrawn and that the present claims are in condition for allowance.

Conclusion

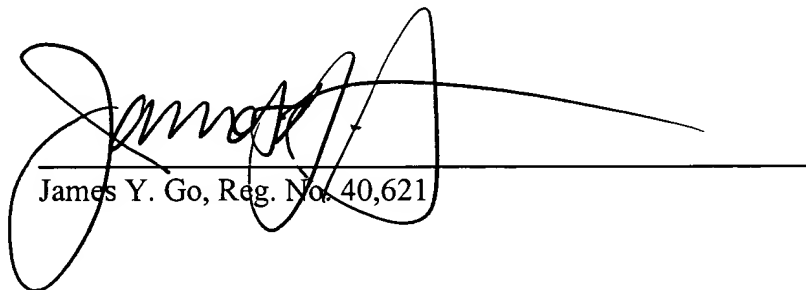
In view of the foregoing, it is believed that all claims now pending patentably define the subject invention over the prior art of record and are in condition for allowance and such action is earnestly solicited at the earliest possible date.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 7-20-01

Customer No. _____
12400 Wilshire Boulevard,
Seventh Floor
Los Angeles, California 90025
(425) 827-8600


James Y. Go, Reg. No. 40,621

FIRST CLASS CERTIFICATE OF MAILING (37 C.F.R. § 1.8(a))

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on: July 20, 2001

Date of Deposit
Rimma N. Oks

Name of Person Mailing Correspondence

Signature

Date 07-20-01

10

VERSION WITH MARKINGS TO SHOW CHANGES MADE

Please amend claims 31, 32, 34 and 36 as follows:

31. (Amended) The power supply controller circuit of claim 29 wherein the control circuit is a pulse width modulation circuit that generates a switching waveform coupled to be received by the power switch to regulate [the]a power supply output.

32. (Amended) The power supply controller of claim 31 wherein the current is representative of a feedback signal from the power supply output, wherein [the]a power supply voltage is regulated through current limit adjustment of the power switch in response to the feedback signal

34. (Amended) The power supply controller of claim 33 wherein the control circuit is to generate [a]said switching waveform controlled in response to the output of the first comparator such that the switching waveform is coupled to limit the current through the power switch.

36. (Amended) The power supply controller circuit of claim 35 wherein the current limit of the current through the power switch is fixed at the predetermined maximum level for magnitudes of the currents that are higher than the current value corresponding to the predetermined maximum [current limit] level.